

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**Applicant : David J. Hathaway, *et. al.*

Docket No. P27064

Appln. No. : 10/709,362

Group Art unit: 2863

Filed : April 29, 2004

Examiner: T.M. Le

**For : SYSTEM AND METHOD OF ANALYZING TIMING EFFECTS OF SPATIAL
DISTRIBUTION IN CIRCUITS****INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents
U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building
401 Dulany Street
Alexandria VA 22314

Sir :

In accordance with the duty of disclosure under 37 C.F.R. § § 1.56 and 1.97-1.99, Applicants hereby call the following to the Examiner's attention.

The documents listed on Form PTO-1449 were found by the Applicants.

Applicants respectfully request that the Examiner consider the materials cited and indicate such consideration by appropriately initialing the enclosed PTO-1449 Form and including a copy of the initialed form in the next official communication.

Applicants note that this Information Disclosure Statement is being filed after receipt of the first action on the merits from the U.S. Patent and Trademark Office and prior to the mailing date of either a final rejection under 37 C.F.R. 1.113 or a Notice of Allowance under 37 C.F.R. 1.311. Accordingly, Applicants hereby authorize the Commissioner to charge the amount of \$ 180.00 and any fees necessary to ensure consideration of the documents cited herein to International Business Machines Deposit

Account No. 09-0456.

10/18/2005 MBEYENE1 00000086 090456 10709362

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Should there be any questions concerning this application, the Examiner is invited to contact the undersigned at the below listed telephone number.

Respectfully submitted,
David J. Hathaway, et. al.

A handwritten signature in black ink, appearing to be 'Andrew M. Calderon', written over the printed name.

Andrew M. Calderon
Reg. No. 38,093

October 13, 2005
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703-716-1191

P27064.P01

FORM PTO-1449 (Modified)		ATTY. DOCKET NO. P27064	SERIAL NO. 10/709,362
LIST OF PATENTS AND PUBLICATIONS APPLICANT'S INFORMATION DISCLOSURE STATEMENT		APPLICANT: David J. Hathaway, et al	
(Use several sheets if necessary)		FILING DATE: April 29, 2004	GROUP: 2863

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
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FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

		Aseem Agarwal, et al., "Statistical Timing Analysis for Intra-Die Process Variations with Spatial Correlations," ICCAD '03, November 11-13, 2003, pages 900-907.
		Michael Orshansky, et al., "Impact of Spatial Intrachip Gate Length Variability on the Performance of High-Speed Digital Circuits," IBEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 21, No.5, May 2002, pages 544-553.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.